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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Furumiya et al.
Serial No.: 10/600,104

Filed: 06/20/2003

Title: Semiconductor Integrated Circuit
and Method of Manufacturing the
Same

Attorney Docket No.: N18347102E

Group Art Unit: 2826

Examiner: Tran, Tan N.

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

This Information Disclosure Statement is submitted:

- under 37 CFR 1.97(b), or
 - (Within three months of filing national application; or date of entry of international application; or before mailing date of first office action on the merits; or before the mailing date of a first office action on the merits after the filing of a request for continued examination)
- under 37 CFR 1.97(c) together with either a:
 - Certification under 37 CFR 1.97(e), or
 - a \$180.00 fee under 37 CFR 1.17(p), or
 - (After the CFR 1.97(b) time period, but before final action or notice of allowance, whichever occurs first)
- under 37 CFR 1.97(d) together with a:
 - Certification under 37 CFR 1.97(e), and
 - a \$180.00 petition fee set forth in 37 CFR 1.17(p).
 - (Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

Applicant(s) submit herewith an Information Disclosure Citation together with copies, of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

The relevance of the attached references is that this is the closest art of which Applicant is aware.

Applicant submits that the above references taken alone or in combination neither anticipate nor render obvious the present invention. Consideration of the foregoing in relation to this application is respectfully requested.

It is requested that the information disclosed herein be made of record in this application.

Respectfully submitted,



Darryl G. Walker

Attorney/Agent for Applicant(s)

Reg. No. 43,232

Date: June 29, 2005

Telephone No.: 1-408-289-5314

37 CFR §1.8

I hereby certify that this correspondence is being

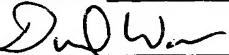
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PTO/SB/08A (08-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known	
Application Number	10/600,104
Filing Date	06/20/2003
First Named Inventor	Furumiya, Masayuki
Art Unit	2826
Examiner Name	Tran, Tan N.
Attorney Docket Number	N18347102E

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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Examiner Initials*	Cite No. 1	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)			
		JP-2001-284533	10-12-2001	Oki Electric Ind Co Ltd	T6
		JP-2002-123915	04-26-2002	Alps Electric Co Ltd	
		JP-2002-025824	01-25-2002	Japan Science & Technology Corp Miyagi Prefecture	
		JP-11-176639	07-02-1999	Fuji Electric Co Ltd	

Examiner Signature		Date Considered	
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Substitute for form 1449/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Application Number	10/600,104
		Filing Date	06/20/2003
		First Named Inventor	Furumiya, Masayuki
		Art Unit	2826
		Examiner Name	Tran, Tan N.
Sheet	2	of	2
		Attorney Docket Number	N18347102E

NON PATENT LITERATURE DOCUMENTS

Examiner Signature		Date Considered	
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